

Docket No.: 61352-043

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
	:	
Takeshi TAKAGI	:	Confirmation Number:
	:	
Serial No.:	:	Group Art Unit:
	:	
Filed: September 09, 2003	:	Examiner:
	:	
For: SEMICONDUCTOR DEVICE	:	

INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

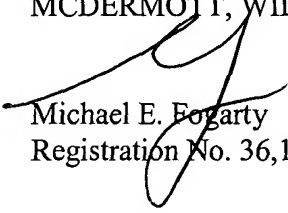
The relevance of each reference listed on attached Form 1449 is discussed in the present specification.

Serial No.:

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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INFORMATION DISCLOSURE CITATION IN AN APPLICATION						ATTY. DOCKET NO. 61352-043		SERIAL NO.	
						APPLICANT Takeshi TAKAGI			
						FILING DATE September 09, 2003		GROUP	
(PTO-1449)									
U.S. PATENT DOCUMENTS									
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ₂ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear				
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FOREIGN PATENT DOCUMENTS									
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes -Number 4 -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation			
						Yes	No		
		JP P2000-260991 A	09/22/2000	UNIV TOKYO		Japanese (w/ English Abstract)			
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)									
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.							
		Toshiro HIRAMOTO et al., "LOW POWER AND LOW VOLTAGE MOSFETs WITH VARIABLE THRESHOLD VOLTAGE CONTROLLED BY BACK-BIAS", IEICE TRANS. ELECTRON., VOL. E83-C, NO. 2 February 2000, pp. 161-169							
		Tadahiro KURODA et al., "A 0.9-V, 150-MHz, 10-mW, 4 mm ² , 2-D DISCRETE COSINE TRANSFORM CORE PROCESSOR WITH VARIABLE THRESHOLD-VOLTAGE (VT) SCHEME", IEEE Journal of Solid-State Circuits, Vol., 31, No. 11, November 1996, pp. 1770-1779							
EXAMINER					DATE CONSIDERED				

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.